Examiner-Initiated Interview Summary	Application No.	Applicant(s)	
	09/480,747	REID ET AL.	
	Examiner	Art Unit	
	Guillermo Munoz	2637	

All Participants:

(1) *Guillermo Munoz*.

(2) *Brian W. Peterman*.

Status of Application: *active*

(3) _____.

(4) _____.

Date of Interview: *9 December 2004*

Time: *10:00*

Type of Interview:

☒ Telephonic
☐ Video Conference
☐ Personal (Copy given to: ☐ Applicant ☐ Applicant's representative)

Exhibit Shown or Demonstrated: ☐ Yes ☐ No
If Yes, provide a brief description:

Part I.

Rejection(s) discussed:

Claims discussed:
Claim 1

Prior art documents discussed:

Part II.

SUBSTANCE OF INTERVIEW DESCRIBING THE GENERAL NATURE OF WHAT WAS DISCUSSED:
See Continuation Sheet

Part III.

☐ It is not necessary for applicant to provide a separate record of the substance of the interview, since the interview directly resulted in the allowance of the application. The examiner will provide a written summary of the substance of the interview in the Notice of Allowability.

☒ It is not necessary for applicant to provide a separate record of the substance of the interview, since the interview did not result in resolution of all issues. A brief summary by the examiner appears in Part II above.

(Examiner/SPE Signature)

(Applicant/Applicant's Representative Signature – if appropriate)

Continuation of Substance of Interview including description of the general nature of what was discussed:
The interview was to locate within the provisional application (US 60145475), support for the limitation "configuring the single integrated circuit to use the asynchronous serial port to transfer data formatted with a synchronous modem transmission protocol between the single integrated circuit and the system-side external circuit through the asynchronous serial port" in claim 1, lines 9-12.
Mr. Peterman indicated the sentence "The modem solution connects at full-duplex rates of up to 2400 bit/s over the public switched telephone network, with V.42 hardware support through HDLC framing." on page 3, lines 1-3; and the sentences "Designed so that it can easily be assimilated into current, as well as new designs, the chip set packs a simple UART interface with flow-control signals. The chips tie directly into a microcontroller or a system RS-232-style serial port" on page 4, lines 1-3, provide the support for this limitation. More specifically, Mr. Peterman indicated that the term "full-duplex" required synchronous communications between the integrated circuit and external system-side circuit.